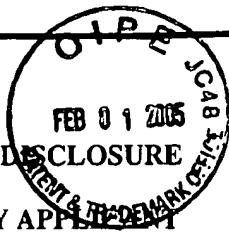


FORM PTO/SB/08A/B (10-01)
Substitute for PTO-1449A/B

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)



Attorney Docket Number	51346/DBP/B600
Application Number	10/694,282
Filing Date	October 27, 2003
Applicant(s)	Ian S. Eslick, et al.
Group Art Unit	2183
Examiner Name	Thomas C. Lee

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ³ (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (<input checked="" type="checkbox"/>)

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
ES		BREBNER, Gordon, "A Virtual Hardware Operating System for the Xilinx XC6200", Field-Programmable Logic and Applications, 6th International Workshop, September 23-25, 1996, pp. 327-336, XP008037568
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Sheet 1 of 1

FORM PTO/SB/08A/B (10-01) Substitute for PTO-1449A/B	Attorney Docket Number	51346/PAN/B600
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	To be Assigned
	Filing Date	Filed Herewith
	Applicant(s)	Ian S. Eslick et al.
	Group Art Unit	To be Assigned
	Examiner Name	To be Assigned

U.S. PATENT DOCUMENTS				
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EC		4,967,340	10-30-1990	Dawes
EC		5,388,215	02-07-1995	Baker et al.
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EXAMINER INITIALS	Cite No. ¹	FOREIGN PATENT DOCUMENT Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
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OTHER DOCUMENTS				
EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
EC		"Smart Compilers Puncture Code Bloat," Brown, Electronic Engineering Times, October 9, 1995 (pages 38 & 42)		
EC		"A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Razdan et al., Micro-27 Proceedings of the 27th Annual International Symposium on Microarchitecture, 11/30 - 12/2/94 (pp. 172-180).		
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EXAMINER SIGNATURE	<i>Eric G.</i>	DATE CONSIDERED	9/6/05
<p>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.pto.gov or MPEP 901.4. ³Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English Language Translation is attached.</p>			

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	Group Art Unit	To be Assigned
	Examiner Name	To be Assigned

OTHER DOCUMENTS		
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